

# New Features & Changes in ScanExpress Software Version 9.0

## Featured Enhancements

### Memory Model Format & GUI Enhancements

Our memory model format has been overhauled to support command sequencing, additional metadata, and improved differential pin handling. These new features allow for more descriptive memory models while at the same time significantly reducing file size and simplifying the process of creating and modifying models for complex memory devices like DDR4 SDRAM. Existing memory models will continue to be supported.

Additionally, a new graphical user interface streamlines the process of creating and updating memory models. A step-by-step wizard assists in package definition, metadata, command definitions, and command sequencing, creating a complete model with minimal data entry required.

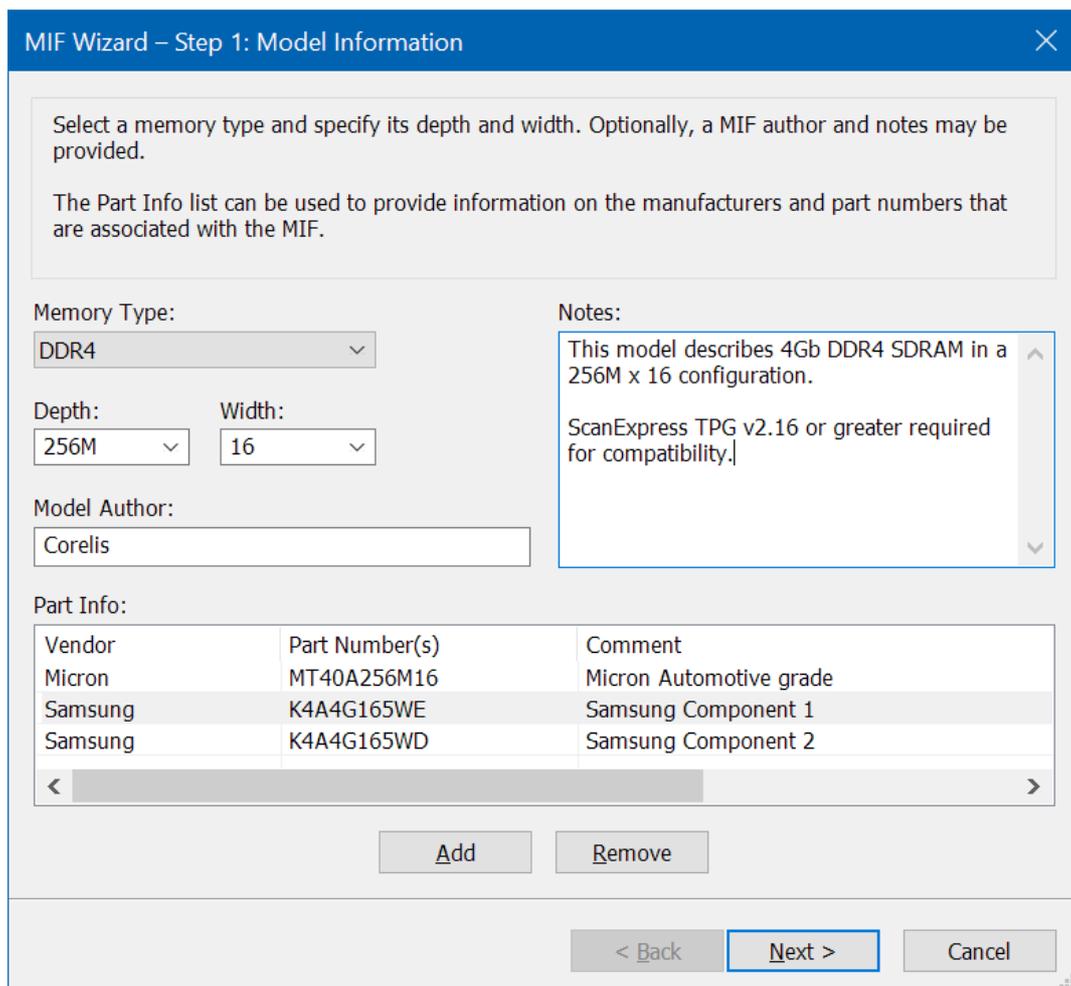


Figure 1. ScanExpress TPG MIF Wizard model information screen.

## ScanExpress Viewer Test Coverage Display

ScanExpress Viewer can now overlay test coverage data on top of CAD and/or photographs for visual analysis of boundary-scan test coverage. Coverage reports created by ScanExpress DFT Analyzer can be imported into ScanExpress Viewer to color-code pins based on “Complete”, “Partial”, “None”, and “Undefined” coverage classifications. Color-coding is customizable and this feature can be used with or without a board photograph.

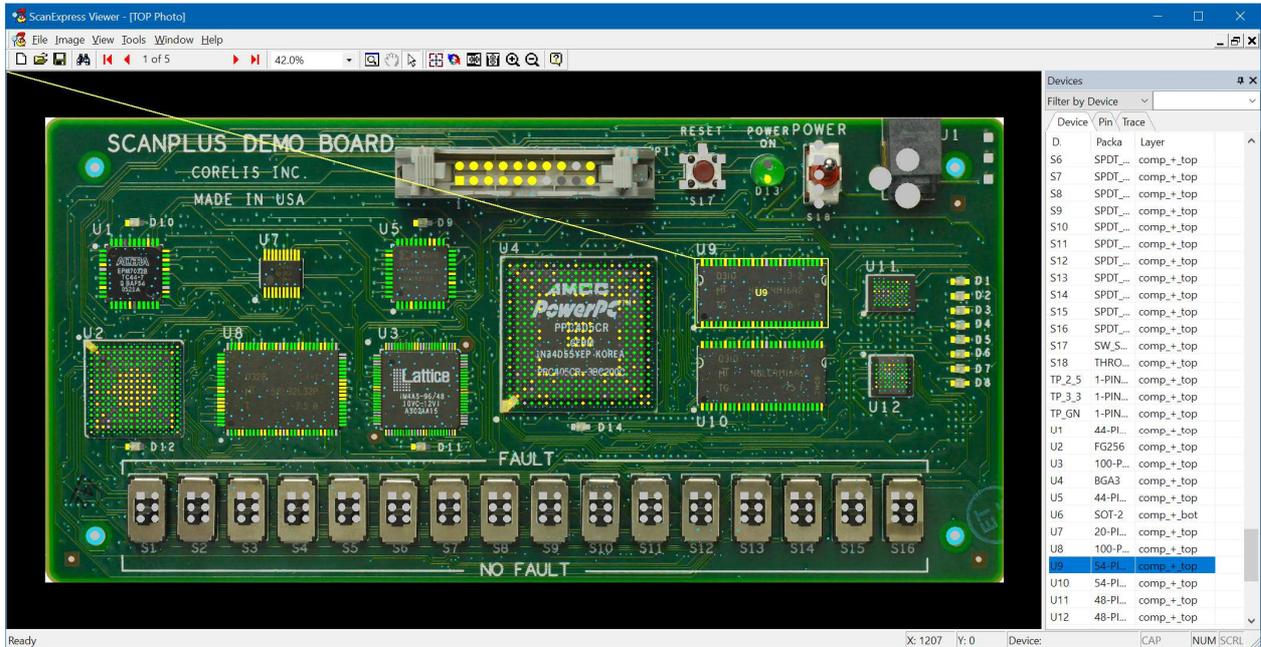


Figure 2. Test coverage data displayed the ScanPlus Demo Board platform.

## IEEE-1149.1-2013 PDL Support

ScanExpress TPG scripts can now call IEEE-1149.1-2013-compliant procedure description language (PDL) files, allowing procedures to be executed from a ScanExpress Runner test plan. Two new script functions, *load\_pdl()* and *tcl\_eval()*, can be used to load and execute PDL or standard TCL scripts. For example scripts and PDL files, browse to *C:\Corelis Examples\ScanExpressTPG\PDL\* after installing ScanExpress TPG.

## ScanExpress JET Zynq NAND Flash Programming

ScanExpress JET and ScanExpress Programmer (Target Assisted Programmer module) now support NAND flash programming with Xilinx Zynq 7000 series All Programmable SoCs. NAND flash support supplements the existing Xilinx Zynq SPI NOR flash programming and basic JET CPU support.

## 64-bit ScanExpress Runner API

ScanExpress Runner now includes both 32-bit and 64-bit DLLs for integration with a wider variety of third-party systems. This means 64-bit versions of popular suites like LabVIEW and MATLAB can call the ScanExpress Runner DLL natively as well as integrate with customer 64-bit applications.

## All Enhancements & Resolved Issues

All ScanExpress software products have been updated with the latest improvements and fixes. The tables below include a complete list of enhancements made and issues resolved with the ScanExpress version 9.0 software release grouped by the most relevant application.

**Note:** Because ScanExpress applications are tightly integrated, some features may require updates to multiple applications to be used. For example, changes to ScanExpress Flash Generator often introduce new flash programming capabilities that require an update to ScanExpress Runner and/or ScanExpress Programmer.

If you were provided a reference number by the technical support group but do not see it listed below, please feel free to contact us and inquire about status.

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### ScanExpress Installation & Licensing

Ref #	Description
<b>3597</b>	Resolved an issue where USB dongle may not be displayed in the License Manager.
<b>3711</b>	Resolved an issue when handling special characters in license request files.
<b>3760</b>	Updated warning message when installing on legacy operating systems.

### ScanExpress DFT Analyzer

Ref #	Description
<b>3147</b>	Added report search capability. The search interface can be launched by pressing CTRL+F or from the right-click context menu.
<b>1252</b>	ScanExpress DFT Analyzer can now automatically open a schematic PDF file and search for a selected net or reference designator.
<b>3520</b>	Added new "Unassigned JTAG Pins Report" to identify testable pins that are not associated with a net.
<b>3386</b>	Fabmaster report generation is now enabled and disabled from the main screen exclusively.

### ScanExpress Flash Generator & Programmer

Ref #	Description
<b>3613</b>	ScanExpress Programmer – TAF now supports custom flash device models.
<b>3612</b>	Added native device model for N25QXX components to ScanExpress Programmer – TAF.
<b>3611</b>	Resolved an error when attempting to run a connection test prior to saving the project in ScanExpress Programmer – TAF.
<b>3475</b>	Added a new option to set RAM size in ScanExpress Programmer – TAF.
<b>3702</b>	Resolve an issue when using the read window with a USB-1149.1/4E controller in ScanExpress Programmer – SPI/I2C.

## ScanExpress Debugger

Ref #	Description
<b>3589</b>	Custom cell definition files will now be copied alongside the referencing BSDL file.
<b>3632</b>	Resolved an issue when modifying group names on the main grid.
<b>3645</b>	Added C100 support to GPIO Programmable Mode.
<b>2341</b>	Passing infrastructure tests will now report more details.
<b>3662</b>	Infrastructure test results will now highlight mismatches between expected and actual values.
<b>3667</b>	Resolved an issue where two error messages were displayed when attempting to open a missing file.
<b>3696</b>	Improved handling of fixed output groups.
<b>3654</b>	Net column and I/O column inside the Pin Browser will now remain aligned when moved.
<b>3562</b>	MCD definitions will now be retained when using the "Editing UUT Info" dialog.
-	Resolved an issue when saving controller configuration settings.

## ScanExpress Runner

Ref #	Description
<b>3592</b>	Improved run time when using the "loop on test step" option with a custom TCK rate.
<b>3594</b>	ScanExpress Runner and ScanExpress Programmer will now treat files with the .srec extension as s-record files.
<b>3629</b>	Resolved an error when using the scan_reset() script command.
<b>3633</b>	Resolved an issue when verifying SPI flash devices at a 1 MHz clock rate.
<b>3621</b>	The ScanExpress Runner DLL will no longer prompt to select data file formats if the format has already been specified in the test plan file.
<b>3682</b>	Diagnostic details view settings are now retained when the dialog is closed and re-opened.
<b>2510</b>	Added a new 64-bit DLL for the ScanExpress Runner API. This DLL can be used with 64-bit applications and third-party test executives such as LabVIEW 2018 (64-bit).
<b>3673</b>	Resolved an issue where SVF file execution would take a very long time.
<b>1137</b>	BLANKCHECK is now a recognized ACTION keyword in STAPL files to support Altera devices.
<b>3705</b>	Added a new test step option to specify delay compensation, allowing test steps with custom TCK rates to be better optimized.
<b>3755</b>	JTAG controllers plugged in during the connection progress dialog will now be discovered immediately.
<b>3690</b>	Controller settings NetUSB-1149.1 controllers using the LAN interface can now be changed without restarting ScanExpress Runner.
<b>588, 1075</b>	JET, TPG script, infrastructure test, and flash output will now be written to the log file regardless of the Advanced Diagnostics selection.

## ScanExpress TPG

Ref #	Description
<b>3570</b>	Resolved an issue when a topology file includes a device that can't be found.
<b>3526</b>	Resolved an issue when using an MCD device with BSDs that define linkage bits that overlap with boundary-scan pins of other BSDs.
<b>3735</b>	Resolved erroneous "The custom device file has changed" message during flash step generation.
<b>2231</b>	All constrained nets will now be included in the "Most Relevant" filter when adding and replacing constraints.
<b>2269</b>	The archive wizard now includes a button to open the containing directory.
<b>3167</b>	The generation status window now includes padding the left edge to improve readability.
<b>3596</b>	Improved the user's manual descriptions of the global coverage level.
<b>3608</b>	Updated the get_date_time() script function example to use a valid month code.
<b>3607</b>	Resolved an issue where DESIGN_WARNING block for IEEE-1149.6-compliant device may not be displayed.
<b>3605</b>	The F1 key will now bring up the help file when the cluster wizard is open.
<b>3610</b>	Script function names in the user's manual are now hyperlinks to their description page.
<b>3258</b>	Added a new filter for "not installed" devices.
<b>3628</b>	Direct I <sup>2</sup> C ACK conditions will now be checked on USB-1149.1/1E and USB-1149.1/4E controllers. If an ACK is not detected, the test will fail.
<b>3064</b>	Added new move up and move down functions to the Scan Chain and MCD screens.
<b>3661</b>	Resolved an issue when importing Accel-EDA format netlists.
<b>2019</b>	The initial infrastructure test in the Script Debugger can now be disabled.
<b>3077</b>	The system-defined color will now be used for description text. This allows custom and high-contrast color modes to be used with ScanExpress TPG.
<b>3601, 3675</b>	Resolved issues when converting IPC-D-356A netlist formats.
<b>3647</b>	Net names will now be analyzed to arrange flash banks in order of LSB to MSB.
<b>3680</b>	Resolved an issue where flash devices would be removed from a bank when replacing the BOM.
<b>3245</b>	The scan chain order will now be maintained when changing BSD files.
<b>2109</b>	Multiple lines can now be selected in the MCD interface.
<b>2110</b>	Improved drag and drop functionality in the MCD wizard.
<b>3685</b>	Resolved issue where generated test steps may not be added to the default test plan.
<b>3660</b>	Script functions can now return arrays.
<b>3086</b>	Resolved an issue when using differential port groups in a script.
<b>2877</b>	Consolidated test step and flash step dialogs. The "Add New" and "Add Existing" functions will now apply to all test steps.
<b>3706</b>	Nets classified as both "POWER" and "NON-DIGITAL" will now automatically be treated as power nets.
<b>1705</b>	Resolved an issue when generating interconnect test that includes single-node nets.
<b>3741</b>	Resolved an issue when pad bits were added to a topology file.
-	Updated and improved memory device model format with condensed command definitions, additional metadata, better differential pin handling, and more.
-	Added a new interface for the creation of memory device models.
-	IEEE-1149.1-2013-compliant PDL can now be executed from ScanExpress TPG scripts.

## ScanExpress JET

Ref #	Description
<b>1786</b>	Improved PMIC support for OMAP3730.
<b>1655</b>	JET test step options will now be copied from the JET project when updating the ScanExpress Runner test plan.
<b>3062</b>	The register initialization utility will now set the controller GPIO signals.
<b>3377</b>	Added string_hex_to_integer() script function for converting strings to integers.
<b>3137</b>	If available, netlists in ScanExpress TPG will be used by ScanExpress JET to generate test coverage reports.
<b>1954</b>	The printf() function will now properly handle escaped percent sign characters. To escape '%' in the output string, use "%%".
<b>3609</b>	Updated manufacturer and device ID for the Micron N25Q064 model.
<b>3622</b>	Added a table of available functions to the user's manual. Each function name is a hyperlink to that function's description.
<b>3631</b>	The device auto detection feature for Zynq7000 can now use a specified base address to look for flash devices.
<b>3636</b>	Canceling the Open Project dialog will no longer clear the open test step list.
<b>3615</b>	Resolved an issue where the find dialog "Up" direction selection was not working properly.
<b>3606</b>	Updated PPC440 definition to remove PCI configuration registers for PPC440GX.
<b>3568</b>	Updated S29GL512N-BYTE model.
<b>3686</b>	Added L2 cache support for MIPS32.
<b>3679</b>	Improved support for Atmel AT91FR40162.
<b>3677</b>	Resolved an issue when programming NOR flash with Altera Excalibur EPXA4.
<b>3692</b>	Resolved an issue where the temporary backup directory may not have been deleted after closing the project.
<b>3691</b>	Resolved an issue where MIPS32 large memory read operations could report incorrect data.
<b>2430</b>	Script functions can now return arrays.
<b>1516</b>	The IDCODE value will now always be displayed in the IDCODE Detect Status dialog, even if a device match is found.
<b>3697</b>	Resolved an issue where target-assisted download could fail on MIPS CPUs.
<b>3688</b>	The get_file_size() and preload_datafile() script functions can now force the file type.
<b>3693</b>	Resolved an issue where MIPS RAM tests could fail on some CPUs.
<b>3725</b>	Controller TCK rate and voltage settings will now be set immediately when a JET project is loaded.
<b>3713</b>	Updated jtag_scan() function example.
<b>3712</b>	Added option to set endianness for ARM9 CPUs.
<b>3769</b>	Resolved an issue when setting a variable to an element of a string array.
-	Added support for NAND flash programming with Zynq7000 CPUs.

## ScanExpress Viewer

Ref #	Description
<b>3577</b>	Resolved an issue where device filters would reset after changing tabs.
<b>2920</b>	Added a new test coverage display option. Report data from ScanExpress DFT Analyzer can now be graphically overlaid on CAD and photograph images to visualize boundary-scan test coverage for a given board.